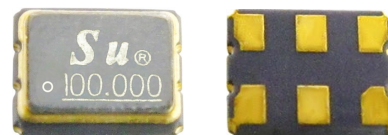


TSQ-3S3225



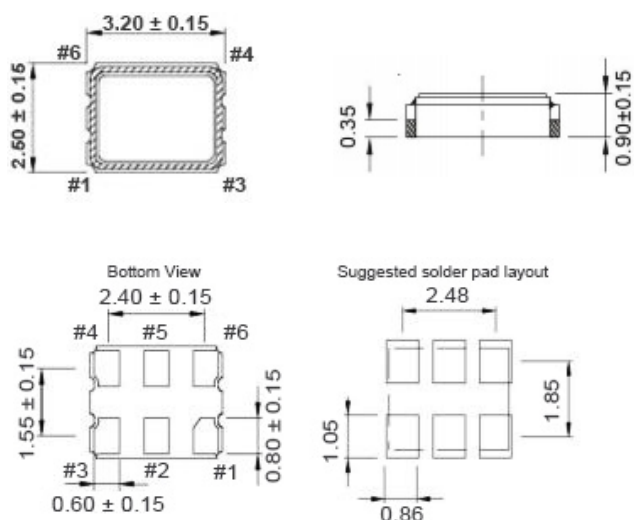
FEATURES

- LVDS LVPECL HCSL
- Wide Frequency Range
- Low Operating Voltage:1.8V/2.5V/3.3V
- Tri-state Available

ELECTRICAL SPECIFICATIONS

Frequency Range	10MHz to 250MHz
Output	LVDS, LVPECL, HCSL
Supply Voltage	1.8V,2.5V,3.3V (±10%)
Frequency Stability	±20ppm
Operating Temperature Range	-40°C ~ +85°C
Current Consumption	100mA max.
Output Load	100Ω between output and complementary output
Symmetry	45% to 55%
Start-up Time	3ms max.
Rise/ Fall Time	1ns max.
Output Enable/ Disable Time	Enable:10ms max. Disable:0.2μs max.
Output Logic High "1"	70%VDD.
Output Logic Low "0"	30%VDD.
Phase Jitter	0.5psec. @12KHz to 20MHz
Storage Temperature Range	-55°C ~ +125°C
Aging (at 25°C)	±2ppm/year max.

Mechanical Dimensions: Unit: mm



Terminal	Connection
#1	Tri-state or N.C
#2	N.C
#3	GND
#4	Output
#5	Complementary Output
#6	Vcc

Tri-state Function	
Tri-state pin	Output
High or Floating	Active
Low	Hi-Impedance

Note: Specifications are subject to change without notice. For more detail and update, please visit our website.